



FIG. 1
Prior Art

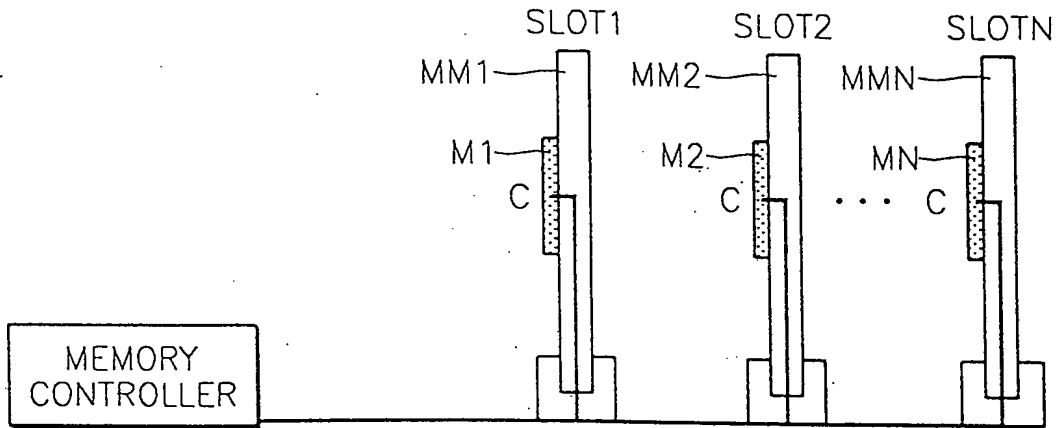
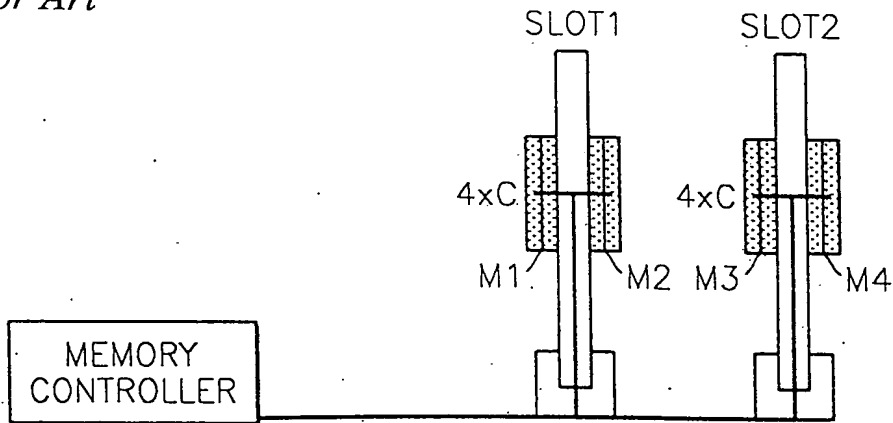


FIG. 2
Prior Art



REPLACEMENT SHEET

MULTI-CHIP PACKAGE FOR REDUCING PARASITIC LOAD OF PIN

Application No.: 10/722,159 – Attorney Docket No.: SEC.1092

Inventors: Byung-Se SO et al.

FIG. 3

Prior Art

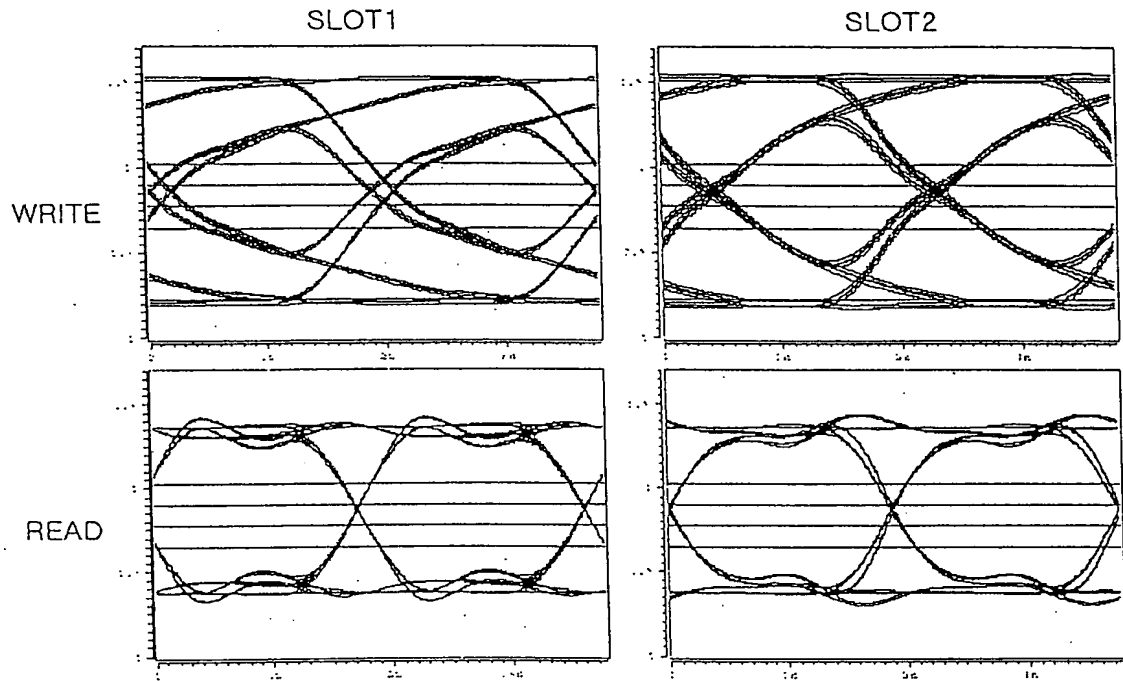
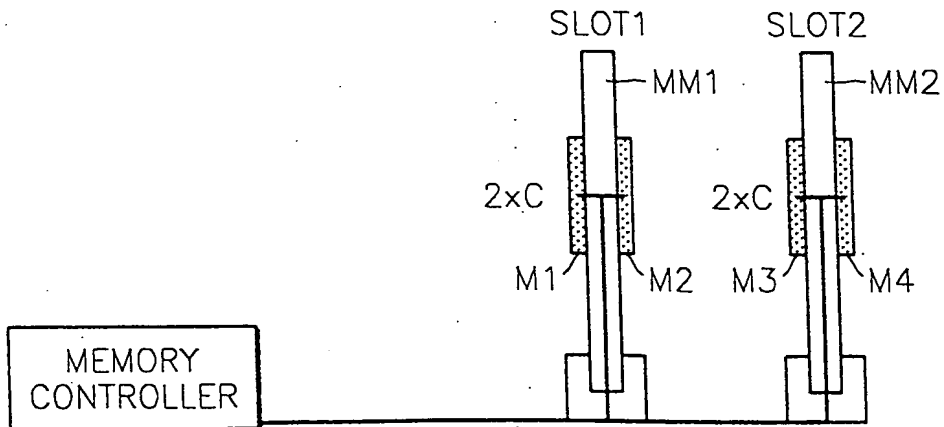


FIG. 4

Prior Art



REPLACEMENT SHEET

MULTI-CHIP PACKAGE FOR REDUCING PARASITIC LOAD OF PIN

Application No.: 10/722,159 – Attorney Docket No.: SEC.1092

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FIG. 5

Prior Art

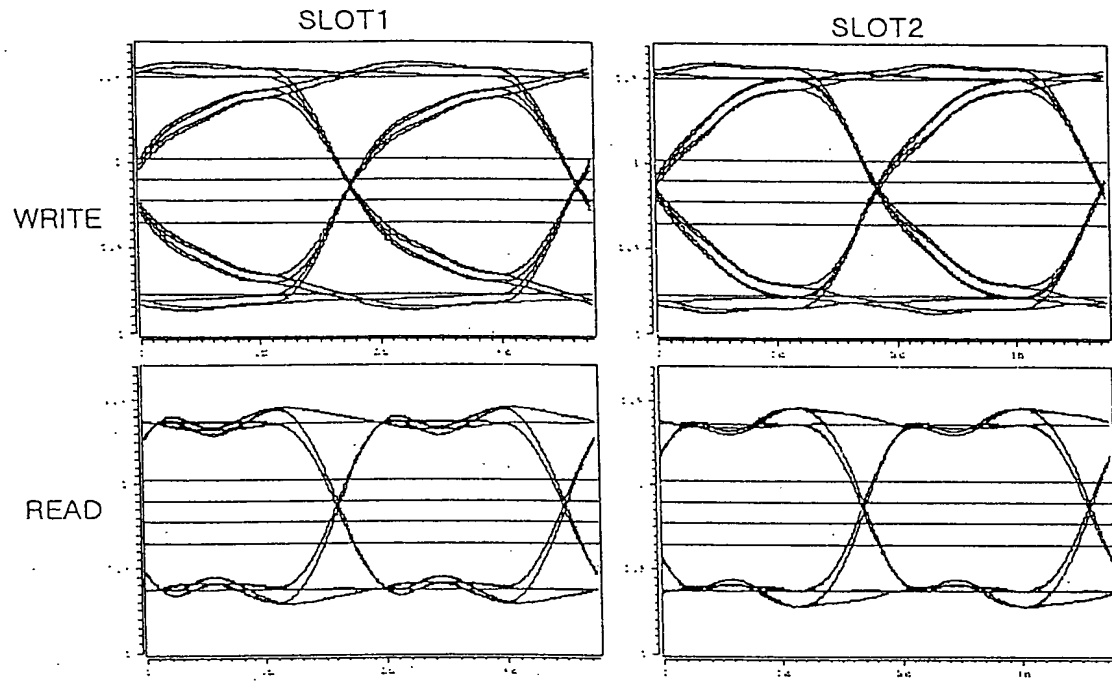


FIG. 6

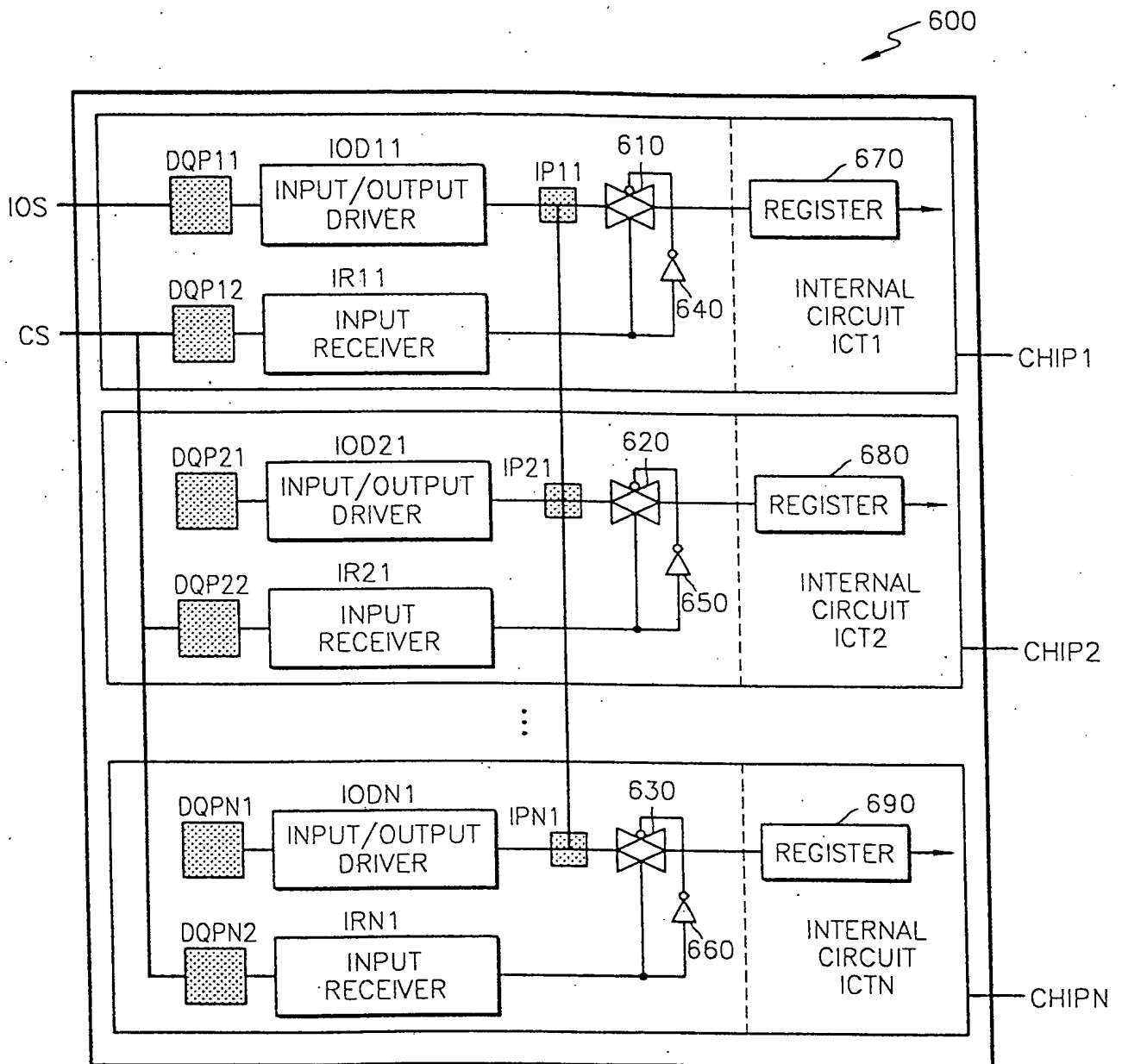


FIG. 7

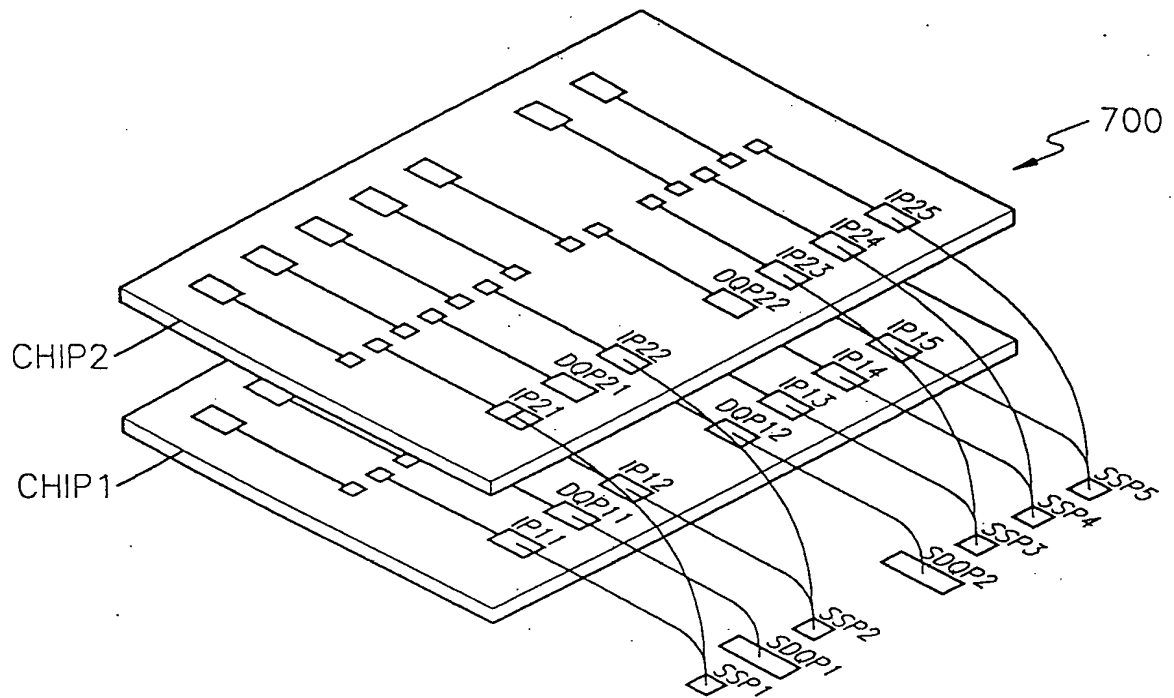


FIG. 8

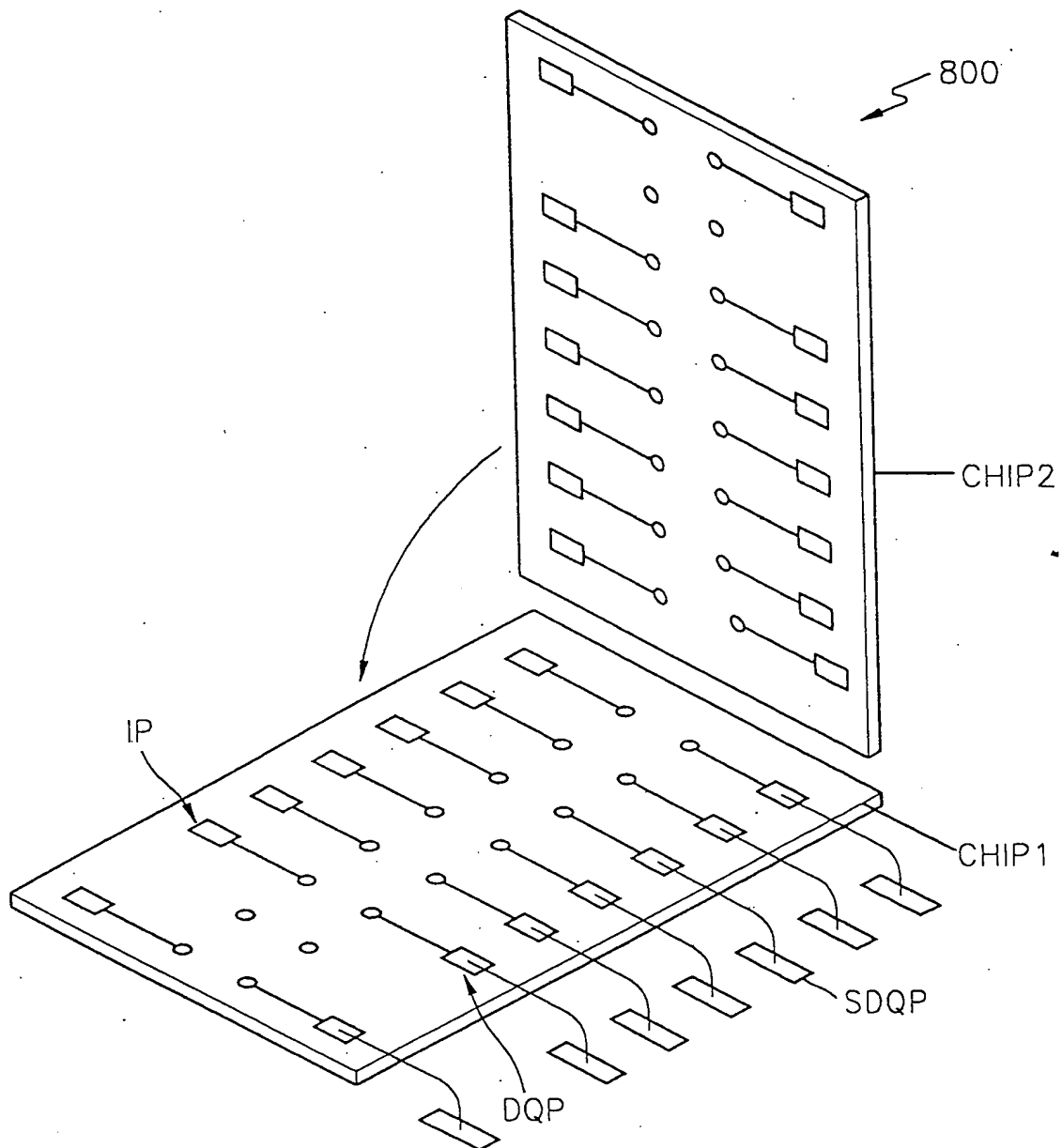


FIG. 9

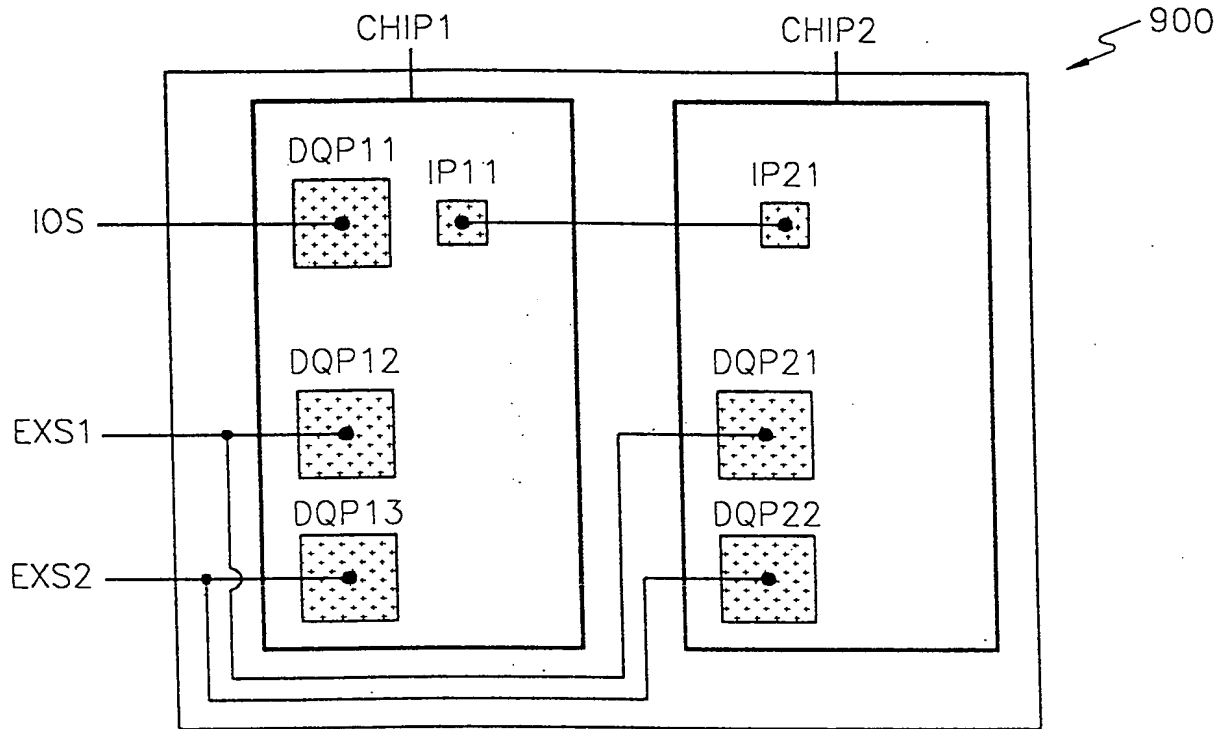


FIG. 10

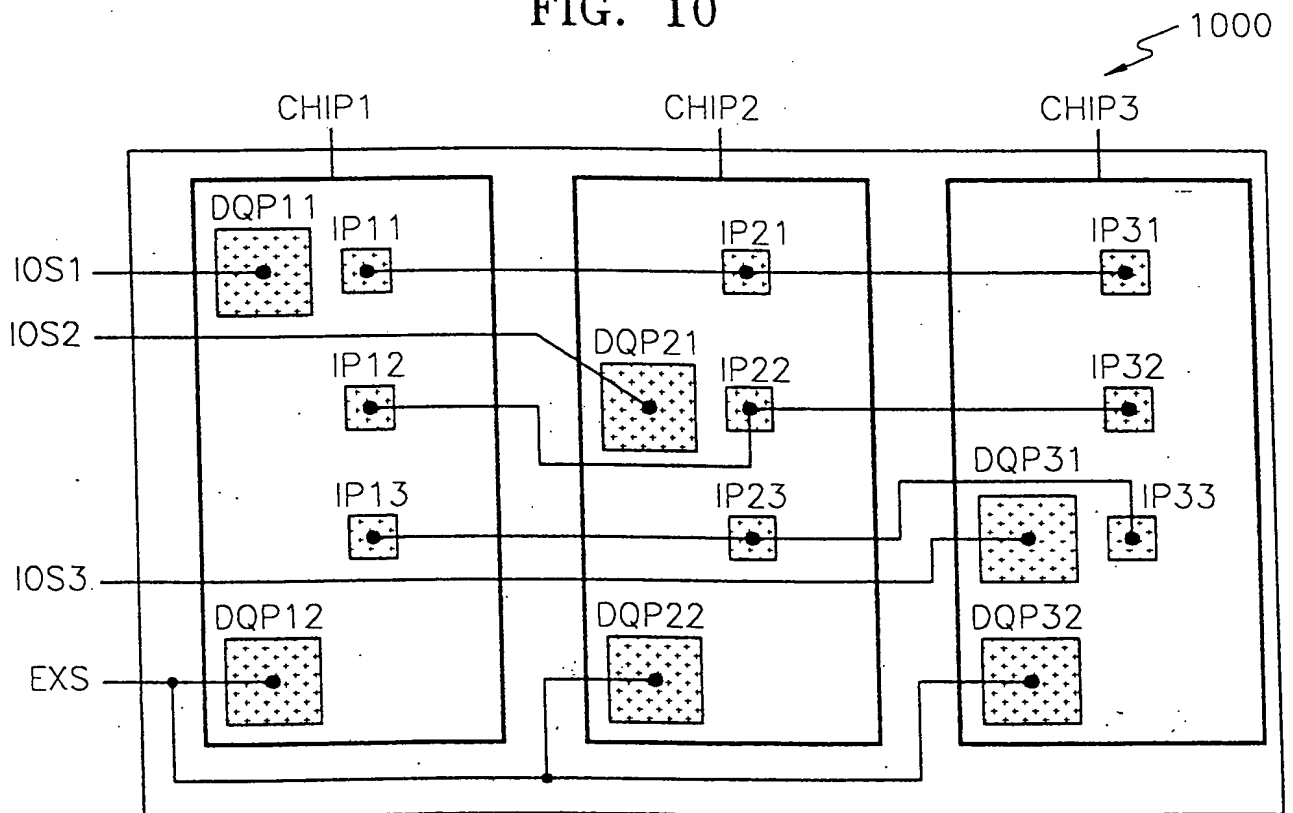


FIG. 11

